

FIG. 1

The diagram illustrates a computer system architecture with the following components and connections:

- 104 (System Boundary):** The outermost dashed line representing the entire system.
- 110 (CPU Core):** A dashed box containing the central processing unit components.
  - 112 (Register File):** A large block with inputs *A*, *T*, and *Di*, and output *S*.
  - 114 (Instruction Cache):** A dashed box containing:
    - 116 (XA):** A 64-bit register.
    - 118 (XB):** A 64-bit register.
    - 120 (XC):** A 64-bit register.
    - 122 (DATA CACHE):** A cache block.
    - 124 (SFT):** A shift register.
  - 126 (MD):** A 64-bit register.
  - 128 (WD):** A write data block.
- 130 (ALU):** An Arithmetic Logic Unit block.
- 132 (X):** A 64-bit register.
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- 198 (X):** A 64-bit register.
- 200 (PC):** Program Counter.
- 202 (I-CACHE):** Instruction Cache.
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- 400 (X):** A 64-bit register.

FIG. 2

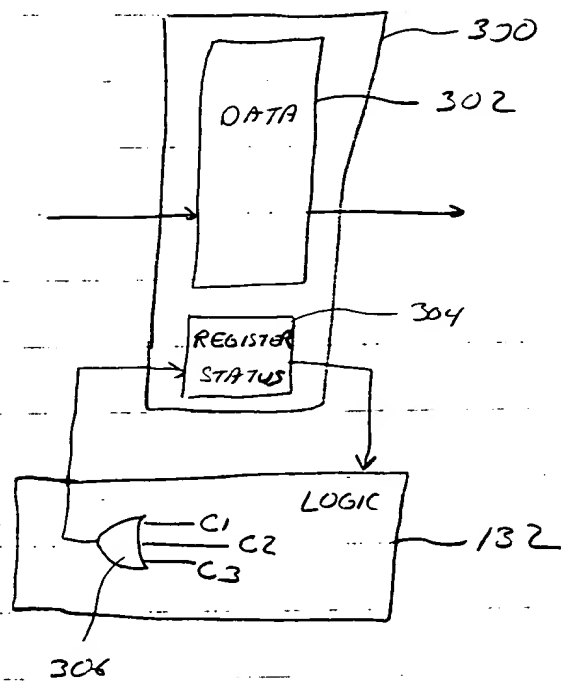
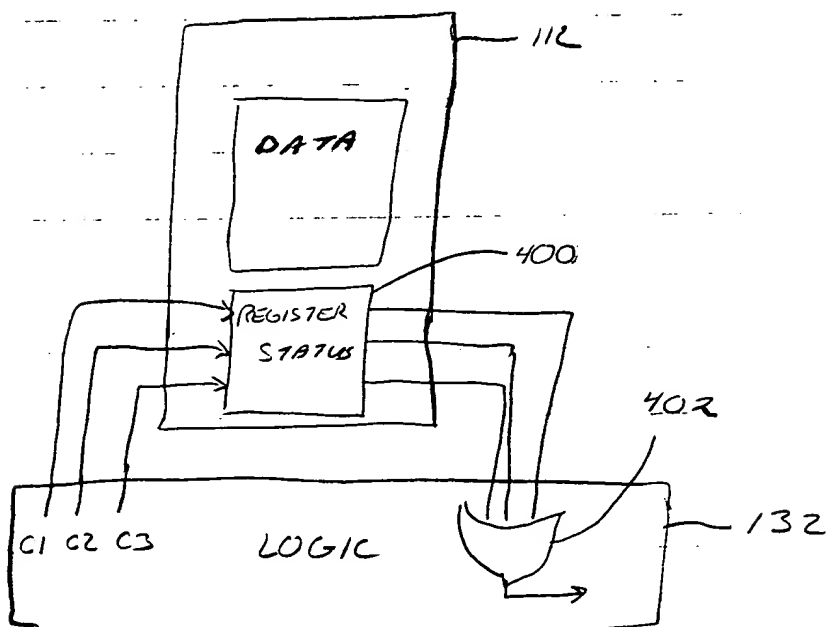
[illegible]

FIG. 3



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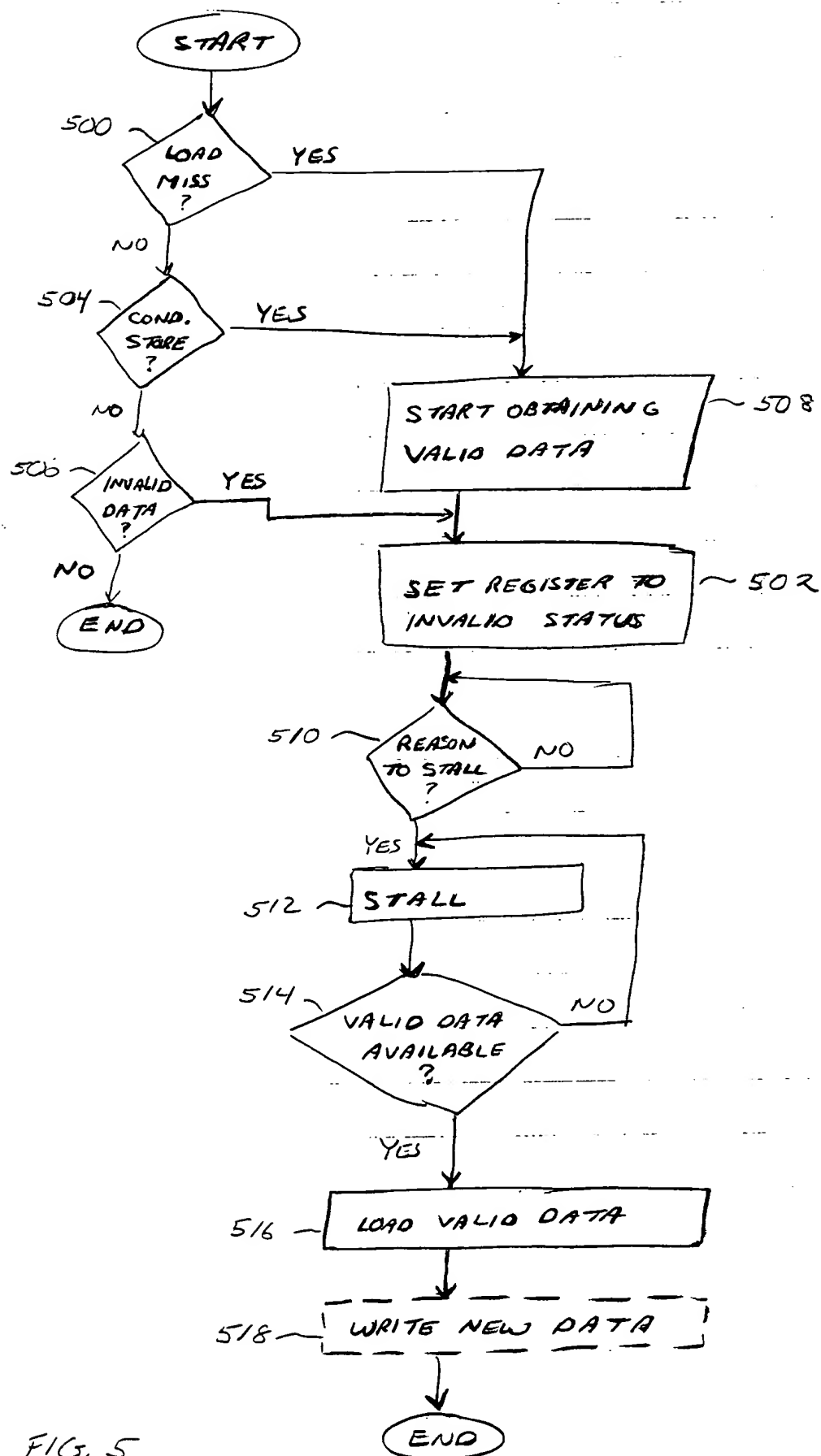


FIG. 5